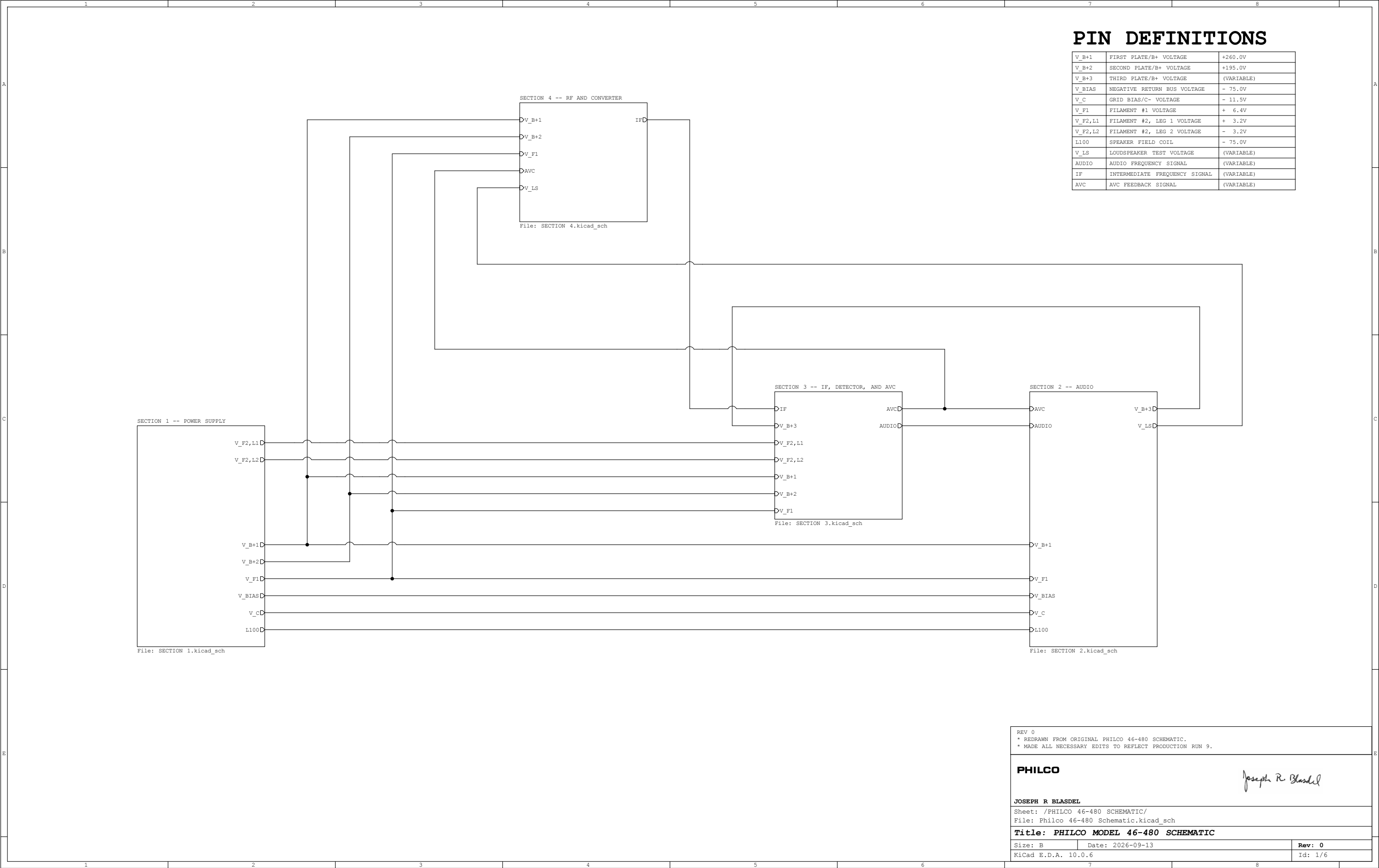




REV 0

* REDRAWN FROM ORIGINAL PHILCO 46-480 SCHEMATIC.

* MADE ALL NECESSARY EDITS TO REFLECT PRODUCTION RUN 9.

PHILCO

JOSEPH R BLASDEL

Sheet: /PHILCO 46-480 SCHEMATIC/

File: Philco 46-480 Schematic.kicad_sch

Title: PHILCO MODEL 46-480 SCHEMATIC

Size: B

Date: 2026-09-13

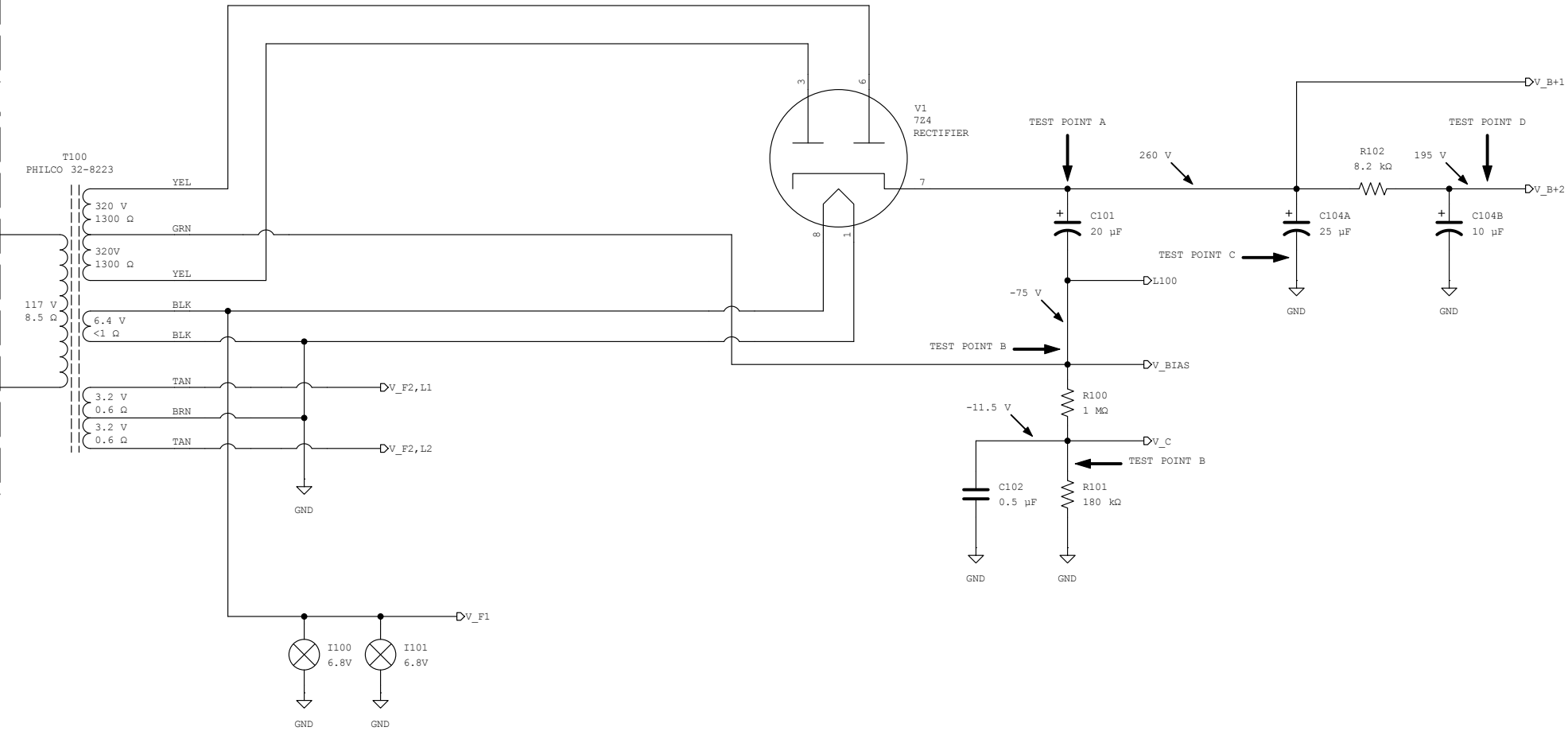
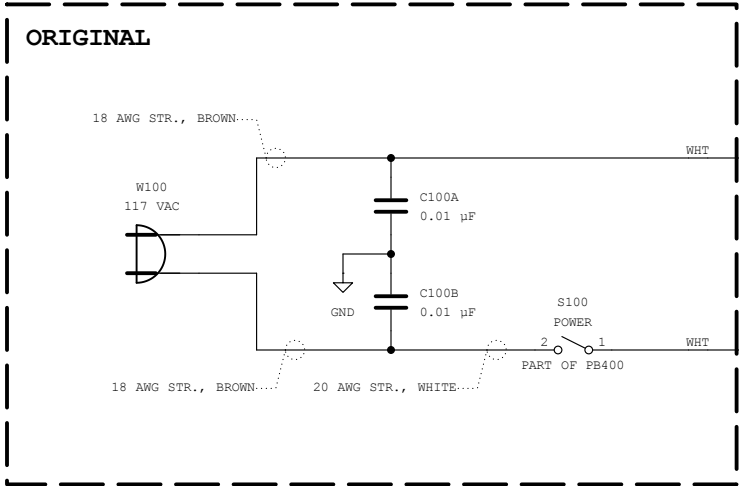
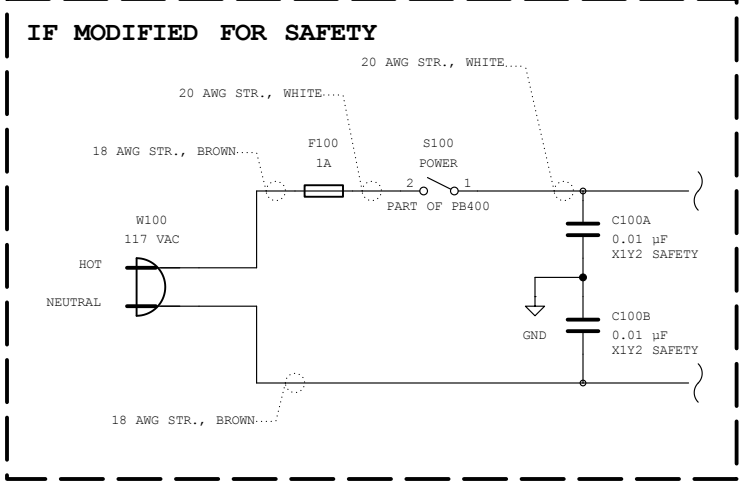
Rev: 0

KiCad E.D.A. 10.0.6

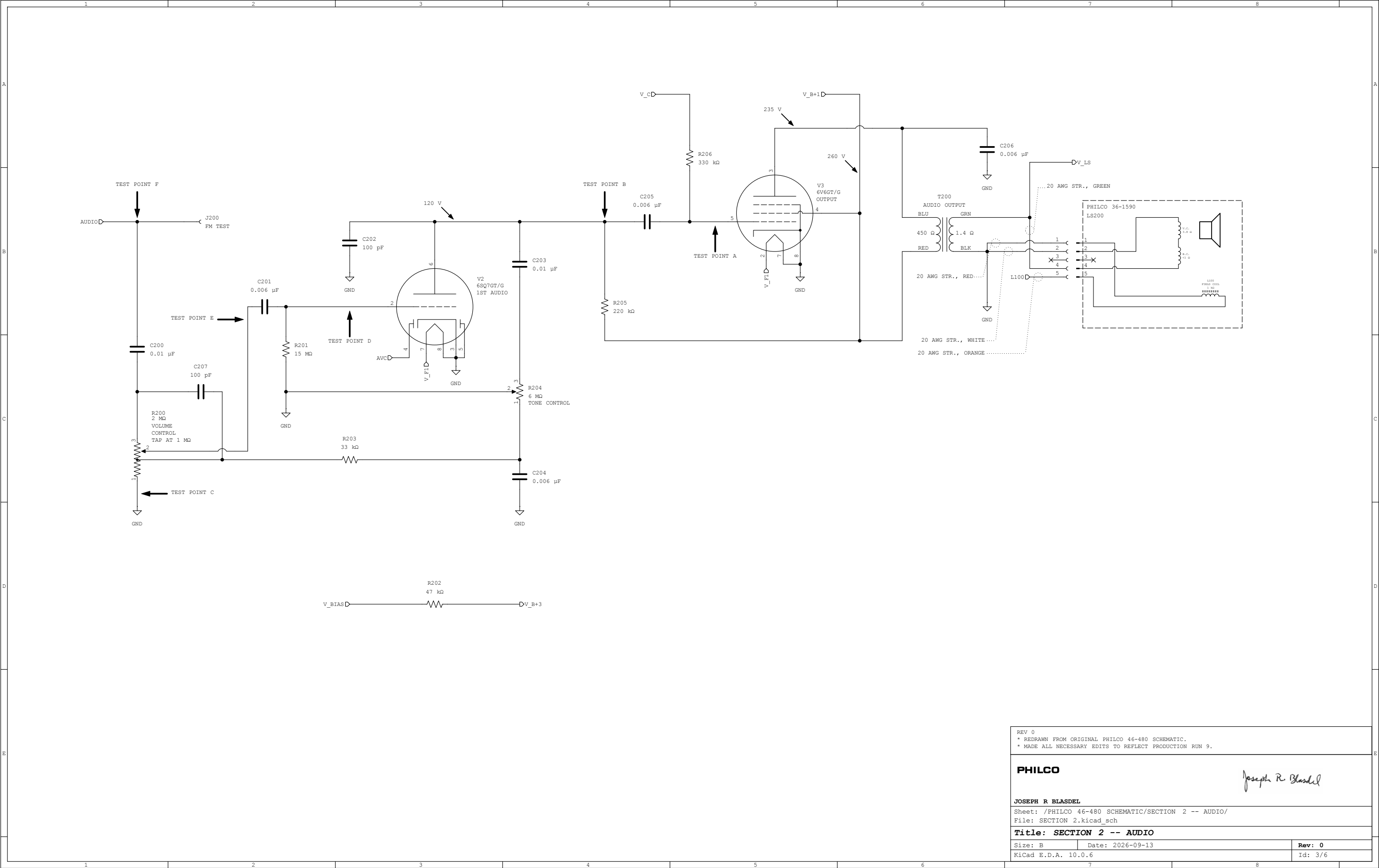
Id: 1/6

PIN DEFINITIONS

V_B+1	FIRST PLATE/B+ VOLTAGE	+260.0V
V_B+2	SECOND PLATE/B+ VOLTAGE	+195.0V
V_B+3	THIRD PLATE/B+ VOLTAGE	(VARIABLE)
V_BIAS	NEGATIVE RETURN BUS VOLTAGE	- 75.0V
V_C	GRID BIAS/C- VOLTAGE	- 11.5V
V_F1	FILAMENT #1 VOLTAGE	+ 6.4V
V_F2,L1	FILAMENT #2, LEG 1 VOLTAGE	+ 3.2V
V_F2,L2	FILAMENT #2, LEG 2 VOLTAGE	- 3.2V
L100	SPEAKER FIELD COIL	- 75.0V
V_LS	LOUDSPEAKER TEST VOLTAGE	(VARIABLE)
AUDIO	AUDIO FREQUENCY SIGNAL	(VARIABLE)
IF	INTERMEDIATE FREQUENCY SIGNAL	(VARIABLE)
AVC	AVC FEEDBACK SIGNAL	(VARIABLE)



REV 0 * REDRAWN FROM ORIGINAL PHILCO 46-480 SCHEMATIC. * MADE ALL NECESSARY EDITS TO REFLECT PRODUCTION RUN 9.		
PHILCO JOSEPH R BLASDEL Sheet: /PHILCO 46-480 SCHEMATIC/SECTION 1 -- POWER SUPPLY/ File: SECTION 1.kicad_sch Title: SECTION 1 -- POWER SUPPLY		
Size: B	Date: 2026-09-13	Rev: 0
KiCad E.D.A. 10.0.6		Id: 2/6



REV 0
* REDRAWN FROM ORIGINAL PHILCO 46-480 SCHEMATIC.
* MADE ALL NECESSARY EDITS TO REFLECT PRODUCTION RUN 9.

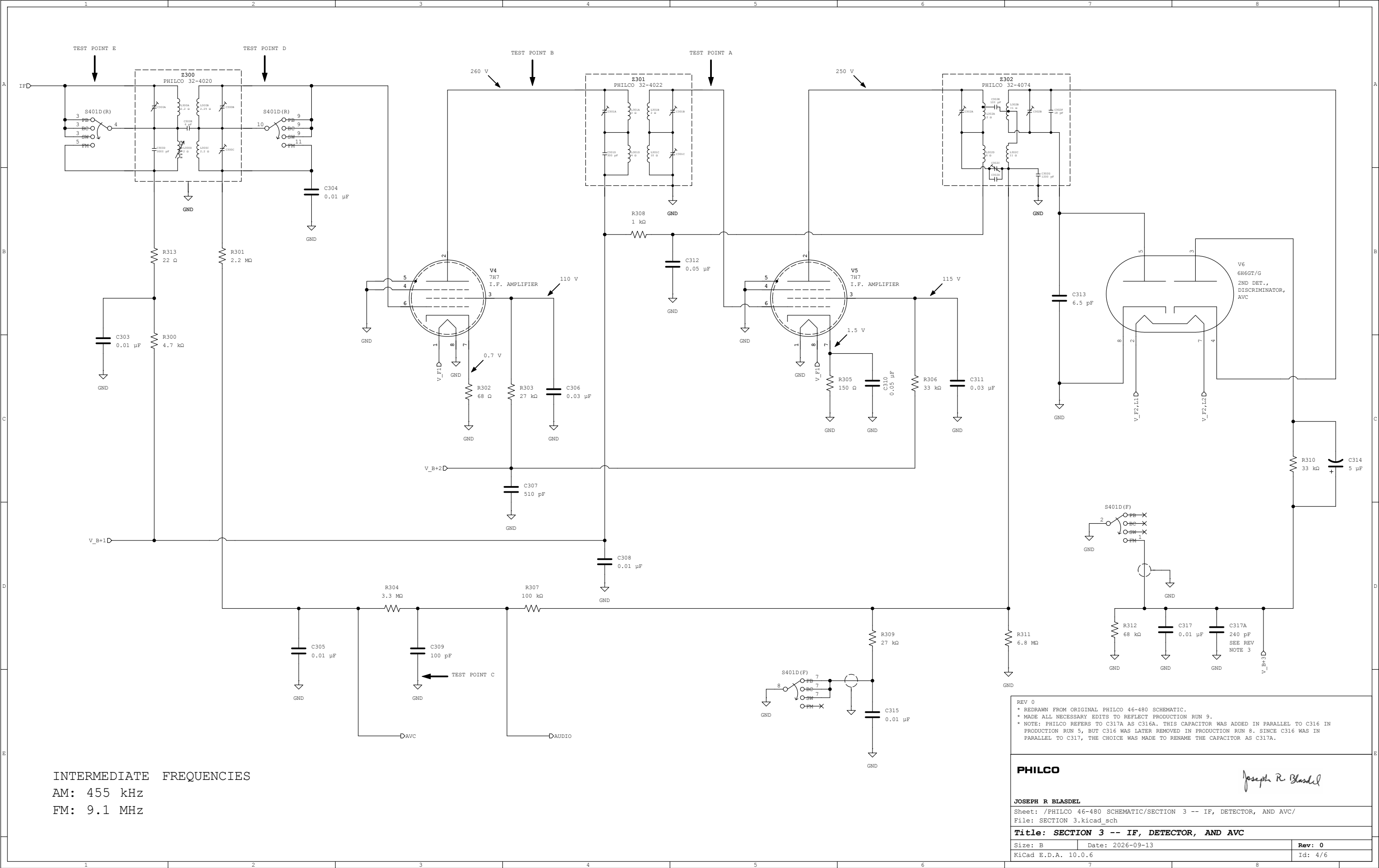
PHILCO

JOSEPH R BLASDEL

Sheet: /PHILCO 46-480 SCHEMATIC/SECTION 2 -- AUDIO/
File: SECTION 2.kicad_sch

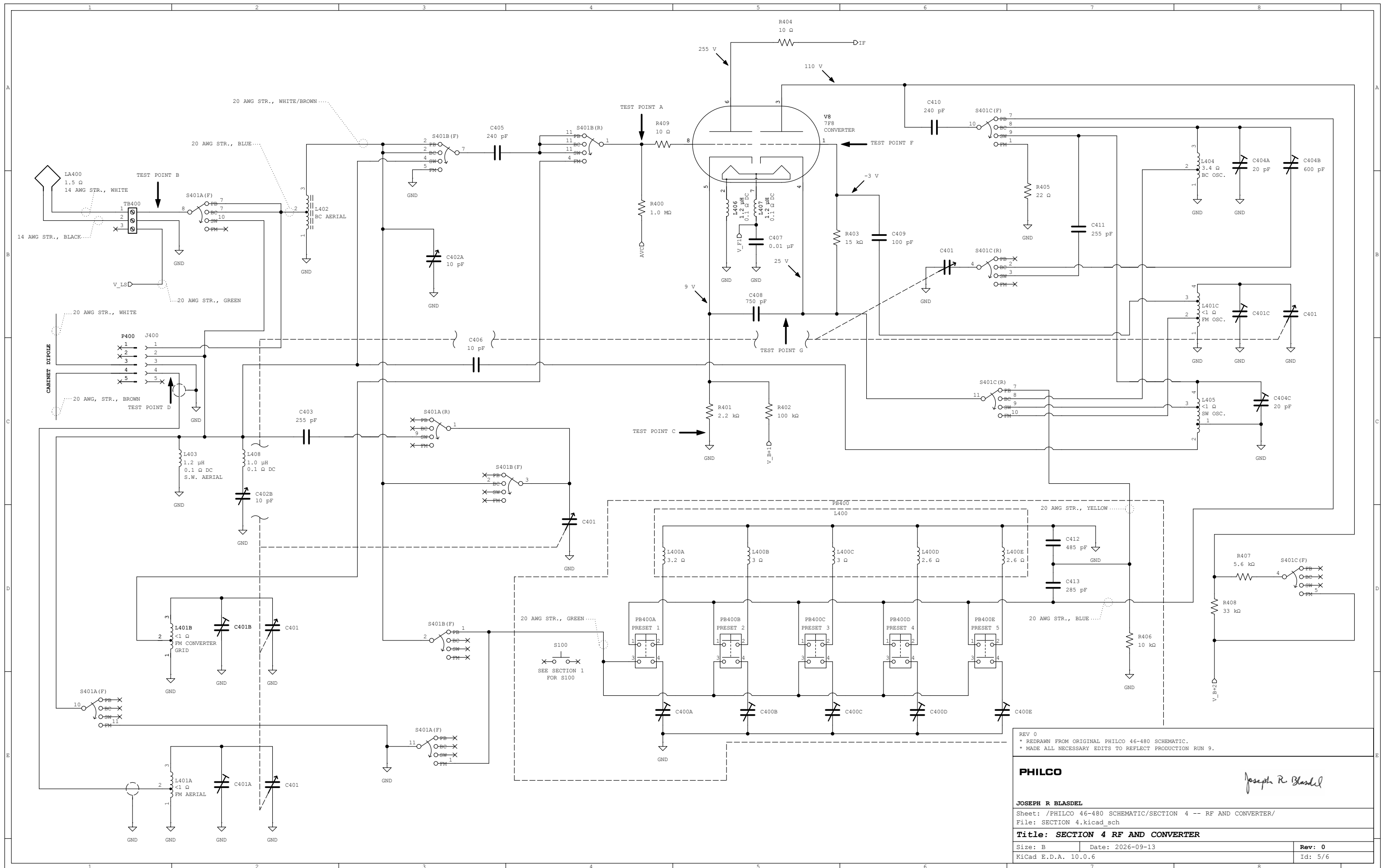
Title: SECTION 2 -- AUDIO

Size: B	Date: 2026-09-13	Rev: 0
KiCad E.D.A. 10.0.6		Id: 3/6



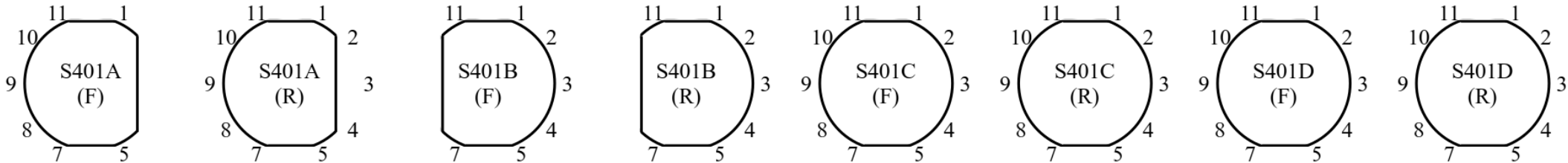
INTERMEDIATE FREQUENCIES
AM: 455 kHz
FM: 9.1 MHz

REV 0
* REDRAWN FROM ORIGINAL PHILCO 46-480 SCHEMATIC.
* MADE ALL NECESSARY EDITS TO REFLECT PRODUCTION RUN 9.
* NOTE: PHILCO REFERS TO C317A AS C316A. THIS CAPACITOR WAS ADDED IN PARALLEL TO C316 IN PRODUCTION RUN 5, BUT C316 WAS LATER REMOVED IN PRODUCTION RUN 8. SINCE C316 WAS IN PARALLEL TO C317, THE CHOICE WAS MADE TO RENAME THE CAPACITOR AS C317A.



SYMBOL LEGEND

	ANTENNA, LOOP STYLE		CAPACITOR, VARIABLE, GANGED		POTENTIOMETER
	ANTENNA, DIPOLE STYLE		CONNECTOR, AC MAINS		POTENTIOMETER WITH HARD TAP
	CABLE		CONNECTOR, FIVE-PIN		RESISTOR
	CABLE, GROUNDED SHIELD		CONNECTOR, FIVE-SOCKET		SPEAKER
	CABLE, OVERLAP WITH JUNCTION		FUSE		SWITCH, GANGED PUSHBUTTON
	CABLE, OVERLAP WITH NO JUNCTION		GROUND, CHASSIS		SWITCH, PUSHBUTTON
	CAPACITOR, UNPOLARIZED		INDUCTOR, AIR CORE		SWITCH, ROTARY WAFER
	CAPACITOR, POLARIZED		INDUCTOR, FERRITE CORE		SWITCH, SINGLE-POLE SINGLE-THROW
	CAPACITOR, TRIMMER		LAMP		TERMINAL BLOCK, THREE-POSITION
	CAPACITOR, VARIABLE		NO CONNECTION		TRANSFORMER



S401 BAND SWITCH WAFER SECTIONS – (F) INDICATES FRONT OF WAFER – (R) INDICATES REAR OF WAFER, AS SEEN THROUGH FRONT

REV 0
* REDRAWN FROM ORIGINAL PHILCO 46-480 SCHEMATIC.
* MADE ALL NECESSARY EDITS TO REFLECT PRODUCTION RUN 9

PHILCO

Sheet: /SYMBOL LEGEND/
File: symbol legend.kicad sch

Title: SYMBOL LEGEND

Size: B	Date: 2026-09-13
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Rev: 0
Ed: 6/6
